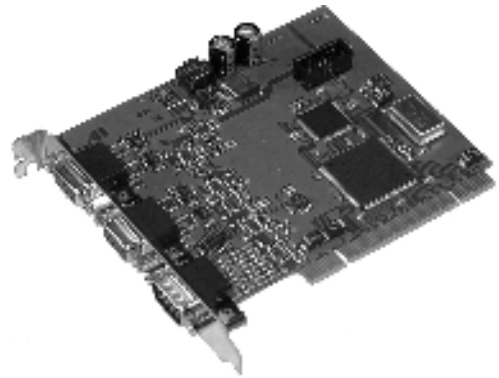


■ **Synchron-Serial-Interface SSI 1417**

Highlights

- 2 Synchron-Serial-Interfaces, max. 44 Bit, 5 MHz, Master- / Slave-Mode
- Timed Data Recording
- Trigger And Zero Setting Input Channels
- Option: Optical Isolated Inputs
- Option: Encoder Supply Voltages
5 V / 300 mA, 12 V / 125 mA or
24 V / 60 mA for each Encoder
- PCI 2.2



SSI-Interfaces

- 2 SSI-Interfaces
- max. 44 Bit
- Option: Isolated Input Channels

Input Channels

- 2 zero setting input channels (channels can be assigned separately to each encoder)
- 2 trigger input channels (channels can be assigned separately to each encoder)
- RS 422-, PNP- or NPN- input configuration
- Option: Isolated input channels

Timer

- Timer controlled recording of the encoder values
- Programmable interval: 6 μ s to > 200 ms

Software

- Driver and DLL for Windows 9x / ME
- Driver and DLL for Windows NT4.0 / 2000 / XP
- Driver and DLL for Windows Vista/7/8/10
- Examples for Delphi, VisualBasic, VisualC++, LabView

Ordering Informations

SSI 1417 /	x/	0/	xx	
				Encoder Supply
		0	00	Without Supply
		0	05	Supply Voltage 5 V
		0	12	Supply Voltage 12 V
		0	24	Supply Voltage 24 V
				Input Channel Isolation
		0		Without Isolation
		1		With Isolation

Ordering Example: SSI 1417/0/0/05

SSI-Board without isolation and with encoder supply voltage of 5 V

Technical Specifications

SSI-Interfaces	: 2 x SSI-Interfaces
Resolution	: max. 44 Bit
Clock Output	: max. 5 MHz
Modes	: Master- and Slave-Mode
Input Resistance	: > 10 k Ω
Input Channels	: 2 x Trigger 2 x Zero Setting Input Channels
Configuration	: RS 422, PNP, NPN
Input Level	: 5 V
Encoder Supply	: optional 5 V / 2 x 300 mA, 12 V / 2 x 125 mA or 24 V / 2 x 60 mA
Connectors	: 2 x 9 pol. SUB-D-Connector 1 x 9 pol. SUB-D-Connector
EMV	: EMV-conform to 2014/30/EU
Operating Temperature	: 0 - 50 $^{\circ}$ C
Storage Temperature	: - 25 to +85 $^{\circ}$ C
Dimensions	: 107 x 127 mm

Accessory

To provide an external power supply for the connected encoder(s) a special adapter is available.

Ordering Information

KA 1417

